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Robust PI controller design satisfying gain and phase margin constraints

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# Abstract:

This paper presents a control design algorithm for determining PI-type controllers satisfying specifications on gain margin, phase margin, and an upper bound on the (complementary) sensitivity for a finite set of plants. Important properties of the algorithm are: (i) it can be applied to plants of any order including plants with delay, unstable plants, and plants given by measured data, (ii) it is efficient and fast, and as such can be used in near real-time to determine controller parameters (for on-line modification of the plant model including its uncertainty and/or the specifications), (iii) it can be used to identify the optimal controller for a practical definition of optimality, and (iv) it enables graphical portrayal of design tradeoffs in a single plot (highlighting tradeoffs among the gain margin, complementary sensitivity bound, low frequency sensitivity and high frequency sensor noise amplification).

# SECTION 1. INTRODUCTION

Many methods for tuning PI and PID controllers have been developed and extensive research in design techniques continues driven by the strong use of such controllers in industry. In summarizing the various tuning methods reported, two categories emerge depending on the types of specifications that the design must satisfy. One class of methods emphasizes gain and phase margin specifications, whereas a second class focuses on sensitivity specifications.

Design techniques based on satisfying gain and phase margin specifications include the following. Ho et. al. [1], [2] developed simple analytical formulae to tune PI and PID controllers for commonly used first-order and second-order plus dead-time plant models to meet gain and phase margin specifications. Ho et. al. [3], [4] reported tuning formulae for the design of PID controllers that satisfy both robustness and performance requirements. Crowe and Johnson [5] presented an automatic PI control design algorithm to satisfy gain and phase margin based on a converging algorithm. Suchomoski [6] developed a tuning method for PI and PID controllers that can shape the nominal stability, transient performance, and control signal to meet gain and phase margins.

Gain and phase margin specifications may fail to guarantee a reasonable bound on the sensitivity a very important control design property. This point has been considered by several researchers. Ogawa [7] used the QFT-framework to propose a PI design technique that satisfies a bound on the sensitivity for an uncertain plant. Poulin and Pomerleau [8] developed a PI design methodology for integrating processes that bounds the maximum peak resonance of the closed-loop. The peak resonance constraint is equivalent to bounding the complementary sensitivity, which can be converted to bounding the sensitivity. Cavicchi [9] gave a design method for bounding the sensitivity while achieving desired steady-state performance. The method can also be applied to measured data. However, plant uncertainty is not considered, and the procedure fits a simple compensation structure. Crowe and Johnson [10] reported a design approach to find a PI/PID controller that bounds the sensitivity while satisfying a phase margin condition. Kristiansson and Lennartson [11] emphasized the need to bound the sensitivity and complementary sensitivity. They suggested the use of an optimization routine to design PI and PID controllers with low-pass filters on the derivative gain to optimize for control efforts, disturbance rejection and bound on the sensitivity. They also provided tuning rules for non-oscillatory stable plants and plants with a single integrator. Astrom et. al. [12] described a numerical method for designing PI controllers based on optimization of load disturbance rejection with constraints on sensitivity and weighting of set point response.

Other investigators have pursued research into tuning methods. Yeung et. al. [13] presented a non-trial-and-error graphical design technique for controller design of lead-lag structure that enables simultaneous fulfillment of gain margin, phase margin and crossover frequency.

These papers and many others apply gain and phase margin constraints in finding PI and PID controller designs. Some add limitations on the (complementary) sensitivity. However, there are several differences between approaches reported in the literature and the idea proposed here. First, the approach here bounds the sensitivity of the closed-loop transfer function for all frequencies, not just at the crossover frequencies where the gain and phase margins are satisfied. (It is possible that the gain and phase margin conditions are met with a given PI design, but the sensitivity can be very high.) Second, the approach developed here accounts for plant uncertainty, in that the controller design must satisfy the specifications for a set of plants. Third, the approach presented provides explicit equations to determine the set of all possible controllers. Fourth, with this method it is possible to extract the optimal control design solution for many practical optimization criteria. Fifth, the algorithm can be applied to many types of plants, including continuous and discrete plants, plants with pure delay, non-minimum phase plants, and stable and unstable plants. Sixth, since the algorithm uses explicit equations, and not optimization routines, it is very fast.

# SECTION 2. PROBLEM STATEMENT

Consider an open-loop transfer function, ,

(1)

where is a member of a finite set of plants is a PI controller,

(2)

with integral gain and proportional gain .

The gain margin and phase margin conditions, the typical measures of robustness, are replaced by a condition on the closed-loop sensitivity inequality,

(3)

where the sensitivity bound and the gain uncertainty of the plant, , is in the interval . It can be shown [14] that when arg . rad, then [(3)](https://ieeexplore.ieee.org/document/#deqn3) requires for , and thus the for a given is at least

(4)

Similarly, when , [(3)](https://ieeexplore.ieee.org/document/#deqn3) requires arg arcsin , and thus the is at least

(5)

Inequality [(3)](https://ieeexplore.ieee.org/document/#deqn3) is a more encompassing measure of robustness than and . It places a bound on the sensitivity at all frequencies, not just at the two frequencies associated with the two margins. As an example of the problem of using the and to capture robustness, consider the following open-loop transfer function,

whose and are, respectively, 35 deg and 24 dB for . These values are the minimum margins corresponding to and in [(3)](https://ieeexplore.ieee.org/document/#deqn3). At these and values the phase margin is guaranteed to be at least 35 deg for any gain, *k*, in the interval [1], [10]. However, from a frequency-domain analysis of , the for is calculated to be 13 deg and the is 4 dB. Thus, the margin specifications fail to guarantee the satisfaction of the for all plant gain uncertainties.

The design problem of interest is to find all pairs that satisfy [(3)](https://ieeexplore.ieee.org/document/#deqn3) for all . For plants that include at least one integrator, the sensitivity is proportional to at low frequencies, and for any plant the sensor noise at the plant input is amplified by at high frequencies. As such, it is of particular interest to find the pair for which is maximum, i.e., the controller design corresponding to lowest sensitivity at low frequencies, and its associated lowest .

# SECTION 3. MAIN RESULTS

To determine the values for which the closed-loop system is stable and [(3)](https://ieeexplore.ieee.org/document/#deqn3) is satisfied, consider first the special case of no gain uncertainty, i.e., , and a single plant . Splitting for into its real and imaginary parts,

(6)

and substituting it and [(2)](https://ieeexplore.ieee.org/document/#deqn2) into [(3)](https://ieeexplore.ieee.org/document/#deqn3) gives,

(7)

where . For an pair which is on the boundary region of the allowed values, there exists such that [(7)](https://ieeexplore.ieee.org/document/#deqn7) is an equality. Moreover, since at that particular , [(7)](https://ieeexplore.ieee.org/document/#deqn7) is minimum, its derivative (with respect to *ω*) at the same is zero. Thus,

(8)

where and the dot indicates derivative with respect to . Solving [(8)](https://ieeexplore.ieee.org/document/#deqn8) for gives

(9)

Substituting [(9)](https://ieeexplore.ieee.org/document/#deqn9) into the equality of [(7)](https://ieeexplore.ieee.org/document/#deqn7) gives a fourth-order equation for as a function of ω.

(10)

where1, with ,

The allowed region for a given value can be calculated as follows: For a given solve [(10)](https://ieeexplore.ieee.org/document/#deqn10) for . Noting that has four solutions (for a given ), select the positive real solution for which the resulting closed-loop system is stable and [(3)](https://ieeexplore.ieee.org/document/#deqn3) is satisfied for . Then use [(9)](https://ieeexplore.ieee.org/document/#deqn9) to find its corresponding . Searching over a range of frequencies enables the boundary of the region to be identified. Note that for point on the boundary, one of the following conditions can occur: (i) increasing is inside the region, (ii) decreasing is inside the region, or (iii) neither increasing nor decreasing is inside the region. Thus, for two points, and on the boundary, any and *b* is a pair within the region only if (i) increasing is within the region, (ii) decreasing is within the region, and (iii) there exist no points on the boundary for any .

## Remark 3.1

A PI controller exists if and only if there exists a frequency for which an pair solving [(10)](https://ieeexplore.ieee.org/document/#deqn10) can be found for which the resulting closed-loop system is stable and [(3)](https://ieeexplore.ieee.org/document/#deqn3) for is satisfied. If a PI controller does not exist and is required, try increasing .

## 3.1 Example 1: Simplified DC Motor

Consider the plant,

(11)

which can represent a simplified model of an armature-controlled DC motor with the input being motor current and the output being speed. For this plant

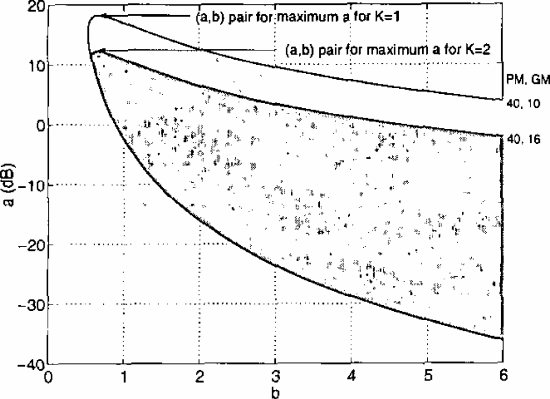
Fig. 1 depicts the values for the particular case of , which is equivalent to a 40 deg or greater and a 10 dB or greater. (The values fall in both shaded regions.) Fig. 1 can also be used to find the values which satisfy any constraint. For example, if 6 dB gain margin uncertainty is desired (that is *K*=2), then for any *b*, the allowed values should be 6 dB less in order to cope with the increase in uncertainty. The region will therefore be the lower shaded region depicted in Fig. 1 where the upper curve is shifted down by 6 dB. The maximum *a* for occurs at and maximum for occurs at giving the controller designs corresponding to lowest sensitivity at low frequencies. Note that if gain uncertainty *K* is required then a solution is guaranteed only if there exists at least a single *b* corresponding to a range of values in an interval such that .

The solution for several values for plant [(11)](https://ieeexplore.ieee.org/document/#deqn11) is depicted in Fig. 2. Each curve is the boundary of the allowed values for a given The corresponding and values indicated are the minimum values along the boundary curve, i.e, the and are equal or greater along the curve.

## 3.2 Example 2: Loaded DC motor

Let the plant be a loaded DC motor whose transfer function from current to speed includes an integrator, a real pole, a complex zero and pole, and a delay, where the complex zero is smaller than the complex pole,

with values rad/sec, and

[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-1-source-large.gif)

**Figure 1:** Region of values for , equivalent to 40 deg phase margin () or greater and 10 dB gain margin or greater ( for ) for Example 1. Lower shaded region is for with additional 6 dB plant gain uncertainty for a total of 16 dB or greater.

The region for is shown in Fig. 3. The pair for maximum is . For these values, the Nichols plot of the open-loop transfer function is shown in Fig. 4. The fact that the curve touches the shaded region confirms satisfaction of inequality [(3)](https://ieeexplore.ieee.org/document/#deqn3).

## 3.3 Extension to Complementary Sensitivity

Replacing the sensitivity margin constraint [(3)](https://ieeexplore.ieee.org/document/#deqn3) by the complementary sensitivity,

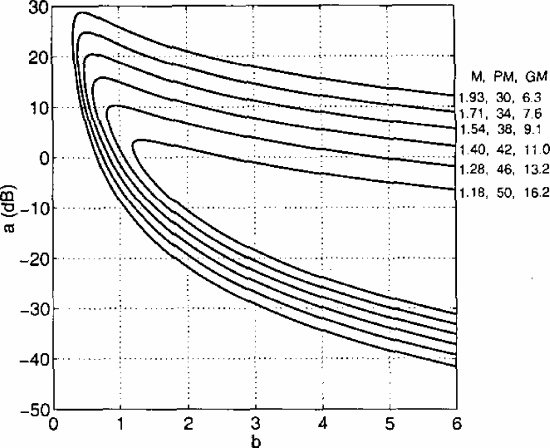
(12)

it can be shown that satisfies [(3)](https://ieeexplore.ieee.org/document/#deqn3) if and only if satisfies [(12)](https://ieeexplore.ieee.org/document/#deqn12). This leads to the following Corollary: If is a pair that solves the problem stated in Section 2, then the pair

solves the Same problem where [(3)](https://ieeexplore.ieee.org/document/#deqn3) is replaced by [(12)](https://ieeexplore.ieee.org/document/#deqn12).

# SECTION 4. OPTIMIZATION

The answer to the question “Which is the best pair?” of course depends on the optimization criterion. Seron and Goodwin [15] note that “In general, the process noise spectrum is typically concentrated at low frequencies, while the measurement noise spectrum is typically more significant at high frequencies.” It follows that an optimal controller can be found by weighting the performance at low frequencies and of noise at high frequencies. Since the high frequency noise is proportional to and low frequency performance to , the optimal solution must lie on the boundary of the curve. Moreover, if there exists more than one boundary pair for the same , the one with the lowest will be the best. The same condition appears in Kristiansson and Lennartson [11] who proposed several evaluation criteria, one being the ability of the system to handle low frequency load disturbance, represented here by parameter .

[[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-2-source-large.gif)](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-2-source-large.gif)

**Figure 2:** Boundary curves of region that satisfy for Example 1. Marked on the right of each curve is its value, minimal phase margin () and minimal gain margin ( in dB for ) according to [(4)](https://ieeexplore.ieee.org/document/#deqn4), and [(5)](https://ieeexplore.ieee.org/document/#deqn5).

## 4.1 Example 1: Continued

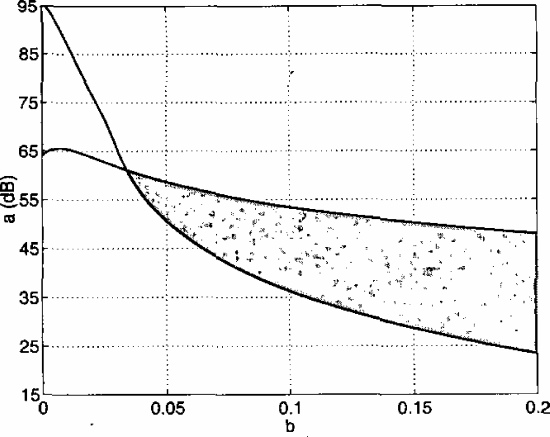
Consider again the plant of Example 1 with the requirement that there be at least 40 deg and at least 6 dB plant gain uncertainty . From a plot of versus *ab*, shown in Fig. 5, and obtained using the same algorithm to generate the plot of Fig. 1, the following conclusions can be drawn.

1. If high frequency sensor noise amplification must be less than 7 dB, then there exists no solution.
2. If high frequency sensor noise amplification greater than 14.7 dB is allowed, then the lowest sensitivity at low frequencies is achieved for .
3. For a required low frequency sensitivity, choose the (*a*,*ab*) pair for which is minimal for the smallest high frequency sensor noise effect.

# SECTION 5. EXTENSION TO UNCERTAIN PLANTS

Assume that the plant, , is known to be one of a finite set of plants, . The controller design challenge here is to find all pairs that solve the problem stated in Section 2 where can be any member of the set. This region will be the intersection of all regions of members of the set (if this intersection region is empty, then there exists no PI solution). As an example, consider the plant set

where and and where as before. Fig. 6 shows the intersection as the shaded region. The pair corresponding to maximum is .

[[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-3-source-large.gif)](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-3-source-large.gif)

**Figure 3:** Region of values for , equivalent to at least 40 deg phase margin and at least 10 dB gain margin for Example 2.

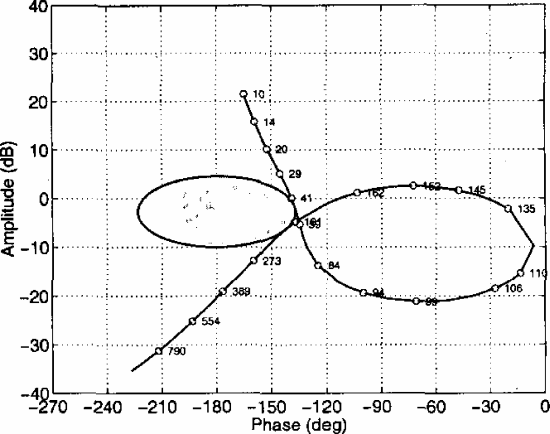
# SECTION 6. DISCRETE PI CONTROLLERS

The problem can be recast in its discrete form, where the plant is and the controller [(2)](https://ieeexplore.ieee.org/document/#deqn2) is replaced by its discrete equivalent,

Using the bilinear transformation,

the plant can be written in the form , the controller in the form,

and the open-loop transfer function in the form,

[[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-4-source-large.gif)](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-4-source-large.gif)

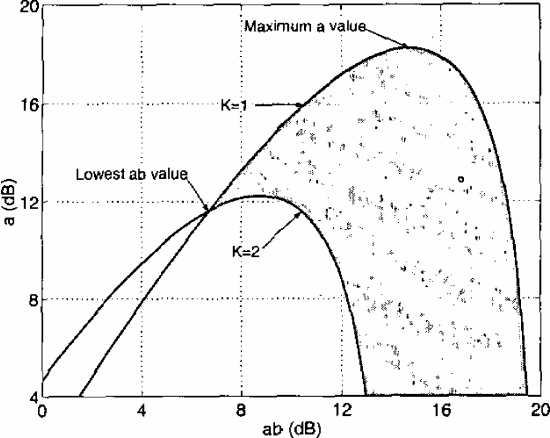
**Figure 4:** Nichols plot for and , corresponding to at least 40 deg and at least 10 dB for Example 2. Frequencies are marked in rad/sec. The open-loop transfer function must not enter the shaded region in order to satisfy the constraint.

The latter three equations translate the discrete problem into the one defined above for finding the (*ad*,*bd*) region. The procedure is as follows: Solve the problem defined in Section [(2)](https://ieeexplore.ieee.org/document/#deqn2) where at the frequencies on the unit circle are replaced by to determine the region. Then will be the region defined by .

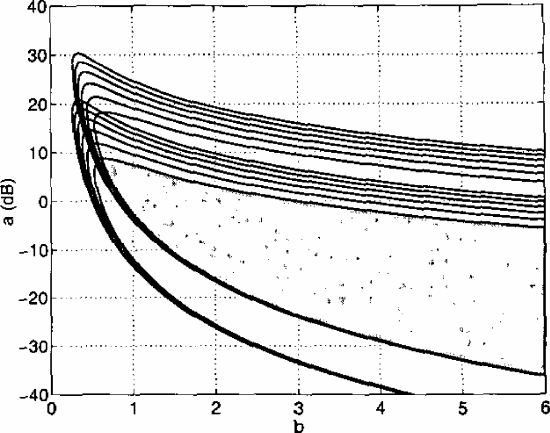
# SECTION 7. CONCLUSIONS

The paper presents explicit equations for calculating PI controllers that simultaneously stabilize a given set of plants and satisfy design specifications, namely and constraints and a bound on the (complementary) sensitivity, for continuous as well as discrete-time systems. The algorithm fits any plant dimension including pure delay. Moreover, the algorithm answers the question if a solution whose bandwidth is in a given interval exists or not.

The two parameters of PI controllers satisfying the constraints correspond to a domain in a plane whose boundary is a curve given explicitly. For a practical optimization criterion presented here, the optimal controller lies on the curve. By inspection, the design plot enables identification of the PI controller for desired robustness conditions, and in particular, gives the PI controller for lowest sensitivity. Tradeoffs among high frequency sensor noise, low frequency sensitivity, and gain and phase margin constraints are also directly available.

[[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-5-source-large.gif)](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-5-source-large.gif)

**Figure 5:** The region, shaded, satisfying where for Example 1.

[[](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-6-source-large.gif)](https://ieeexplore.ieee.org/mediastore_new/IEEE/content/media/8775/27784/1240450/1240450-fig-6-source-large.gif)

**Figure 6:** Boundary curves of region that satisfy for for a set of plants. The intersection of all regions is the allowed region. is equivalent to 40 deg or greater and 10 dB or greater.

The algorithm can be executed very fast for highly uncertain plants, and as such the controller design can be updated in near real-time to reflect changes in plant uncertainty and/or closed-loop specifications.

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